

FIG. 1A

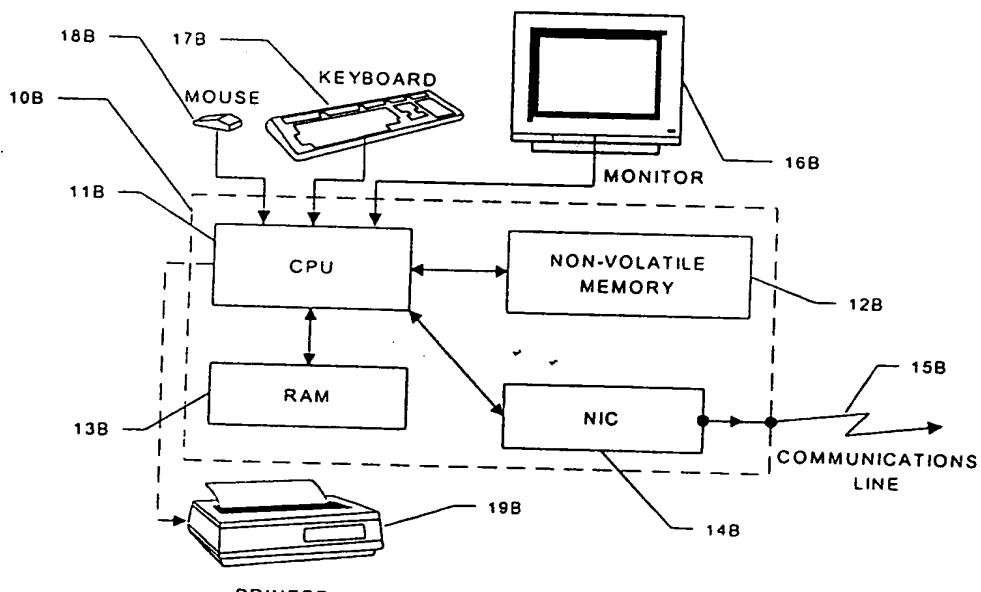
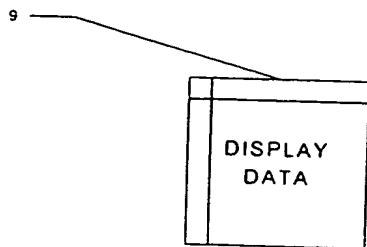


FIG. 1B



Insert memory mappings for volatile and non-volatile memory.

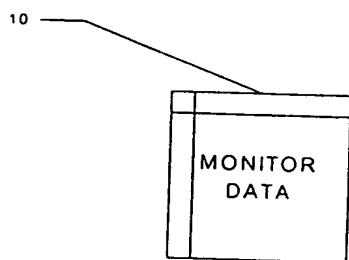


FIG. 1C

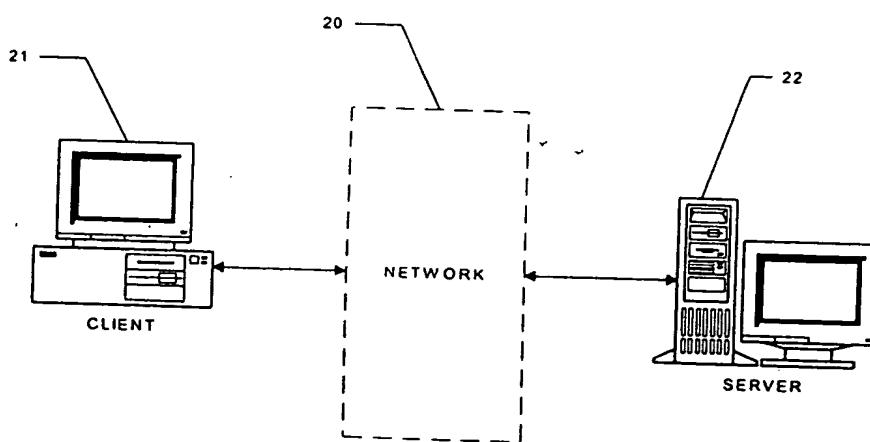


FIG. 1D

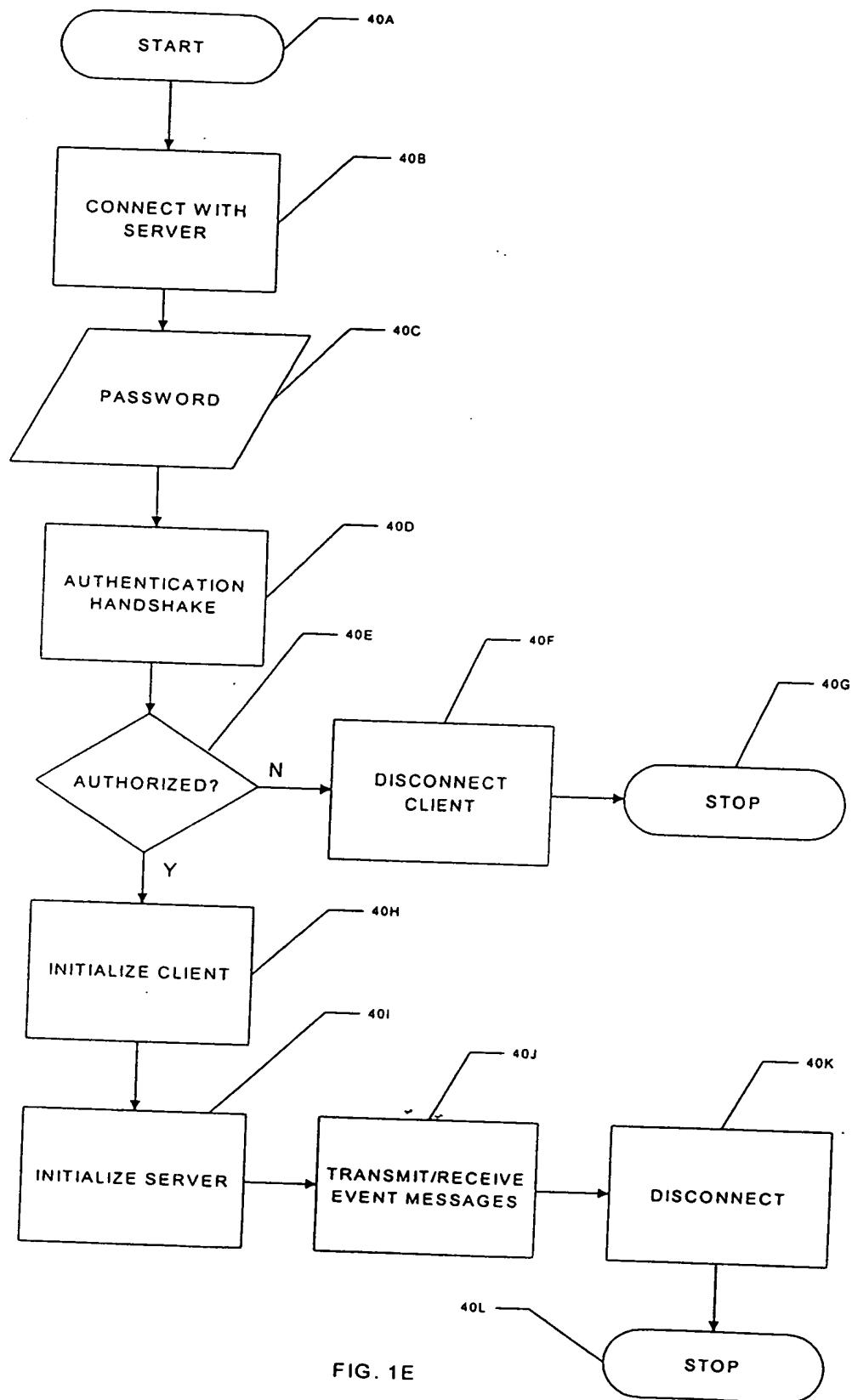


FIG. 1E